

Ye, Haojie

☎ 734.239.3020 | ✉ yehaojie@umich.edu | <https://web.eecs.umich.edu/~yehaojie/>

EDUCATION

Ph.D. Candidate in Computer Science and Engineering

Expected Spring 2024

Thesis Advisor: Prof. Trevor Mudge

University of Michigan, Ann Arbor

- **Overall GPA:** 4.0/4.0
- **Area Focus:** Computer Architecture, Algorithm, and System Design for emerging applications such as Recommendation Systems, Graph-based Machine Learning, and Graph Mining

M.S.E. in Computer Science and Engineering

Sept 2019 – May 2021

Thesis Advisor: Prof. Trevor Mudge

University of Michigan, Ann Arbor

- **Overall GPA:** 4.0/4.0

B.S.E. in Computer Engineering

Sept 2017 – May 2019

University of Michigan, Ann Arbor

PUBLICATION

1. **H. Ye**, Y. Yang, R. Dreslinski, T. Mudge, N. Talati, "DRILL: Improving Recommendation Systems Performance Using Dynamic Reduction Locality" (ISCA'22) *under review*
2. A. Khadem, **H. Ye**, T. Mudge "CoDR: Computation and Data Reuse Aware CNN Accelerator" (DAC'21 poster session)
3. N. Talati, D. Jin, Di, **H. Ye**, A. Brahmakshatriya, G. Dasika, S. Amarasinghe, T. Mudge, D. Koutra, R. Dreslinski, "A Deep Dive Into Understanding The Random Walk-Based Temporal Graph Learning" (IISWC'21)
4. Y. Yang, **H. Ye**, Y. Chen, X. Liu, N. Talati, X. He, T. Mudge, R. Dreslinski. "CoPTA: Contiguous Pattern Speculating TLB Architecture," International Conference on Embedded Computer Systems: Architectures, Modeling and Simulation (SAMOS'20)
5. X. He, S. Pal, A. Amarnath, S. Feng, D. Park, A. Rovinski, **H. Ye**, Y. Chen, R. Dreslinski, T. Mudge. "Sparse-tpu: Adapting systolic arrays for sparse matrices" (ICS'20)

SELECTED PROJECTS

Transmuter - Software-defined Hardware Algorithm Part

Jan 2020 – Current

Dataflow Simulation on Accelerator using gem5 simulator

University of Michigan, Ann Arbor

- **Abstract:** Novel sparse matrix dataflow simulation on Transmuter
- Simulated different sparse matrix operation dataflows on Transmuter using gem5
- Implemented row-wise dataflow and out-perform the previous state-of-the-art outer-product dataflow
- Proposed cooperative systolic mode to further improve the performance and energy efficiency

TECHINICAL SKILLS

- **Programming Languages:** C/C++, Python, CUDA, Verilog HDL, System Verilog, HTML, Matlab
- **Software and Design Tools:** Git, Murphi, Cadence, Design Compiler, Latex
- **Architecture Simulators:** Gem5, Gem5-gpu, DRAMSim, Ramulator, CACTI, Zsim

TEACHING EXPERIENCE

Graduate Student Instructor

Sept 2020 – Jan 2021

EECS 598 Applied Parallel Programming with GPUs

University of Michigan, Ann Arbor

- Working with Prof. Reetuparna Das, preparing lecture slides, homework, and exams in teaching GPU micro-architecture and CUDA programming language to graduate students

AWARDS & HONORS

- Graduate Student Research Assistant (GSRA) at University of Michigan (2020)
- Outstanding Undergraduate Research Award (2019)
- James B. Angell Scholar (2019)

REFERENCES

- Prof. Trevor Mudge, Bredt Family Professor, Computer Science and Engineering, University of Michigan, Ann Arbor, MI. E-mail: tnm@umich.edu
- Prof. Ronald Dreslinski, Assistant Professor, Computer Science and Engineering, University of Michigan, Ann Arbor, MI. E-mail: rdreslin@umich.edu